

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10501651			
<b>Filing Date:</b>	10-Jan-2005			
<b>Title of Invention:</b>	On-chip esd protection circuit for compound semiconductor heterojunction bipolar transistor rf circuits			
<b>First Named Inventor/Applicant Name:</b>	Yin Tat Ma			
<b>Filer:</b>	Daniel Lewis Dawes			
<b>Attorney Docket Number:</b>	UC1.PAU.05US			
Filed as Small Entity				
<b>U.S. National Stage under 35 USC 371 Filing Fees</b>				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				
Extension - 1 month with \$0 paid	2251	1	60	60

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				60